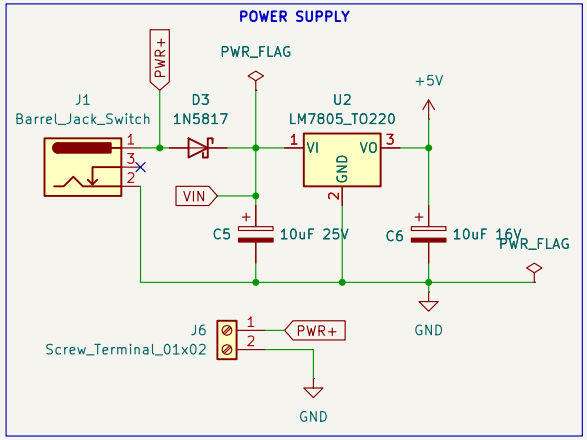
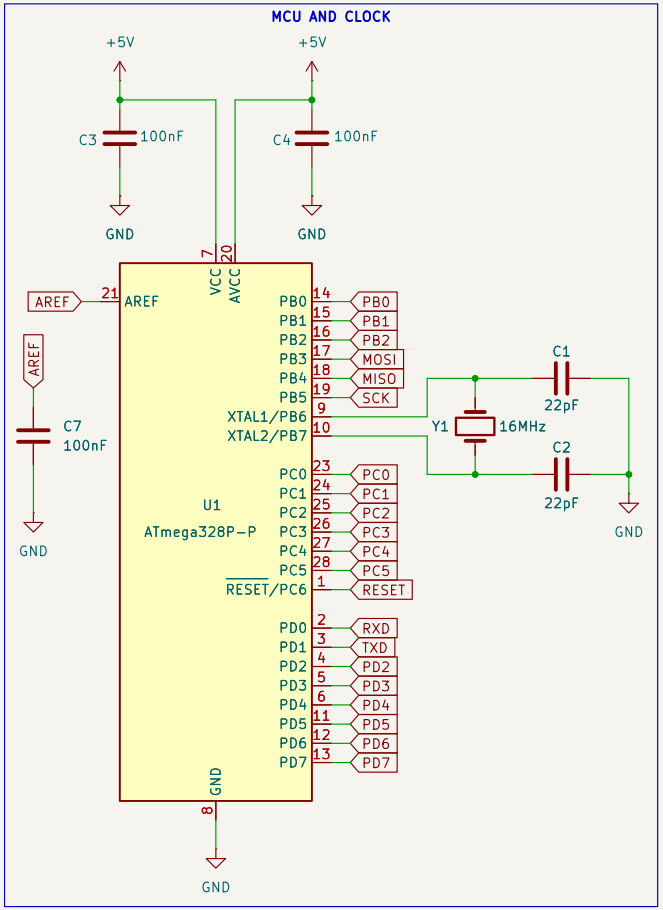
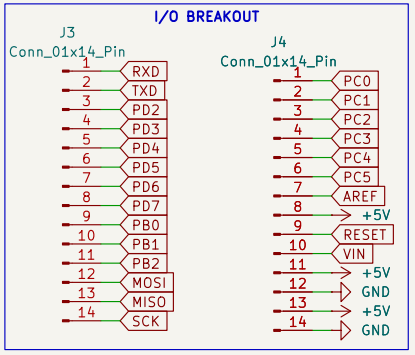
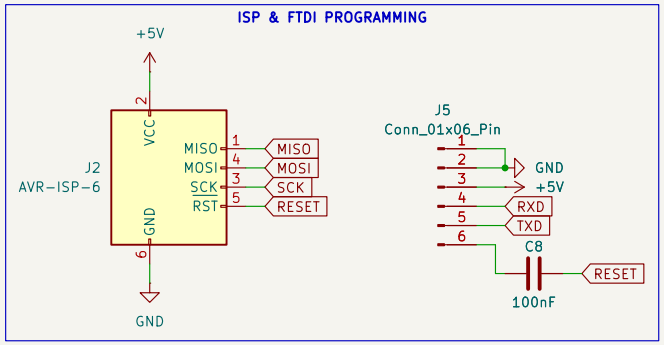
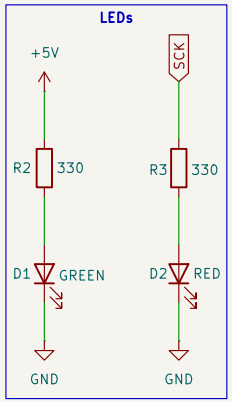
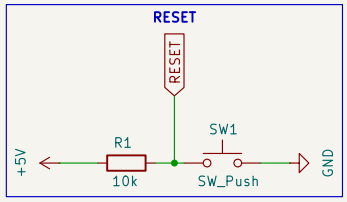












Programming: ISP (J2) and FTDI (J5)
Through-hole, two-layer (F.Cu and B.Cu)
Created by siwit.co/PCB1 Approved by Dr. Sam Macharia
siliconwit.com

Sheet: /
File: atmega328p-breakout.kicad_sch

Title: ATmega328P Breakout Board: Through-Hole PCB

Size: A4	Date: 2026-11-17	**Rev: B**
KiCad E.D.A. 10.0.8		Id: 1/1